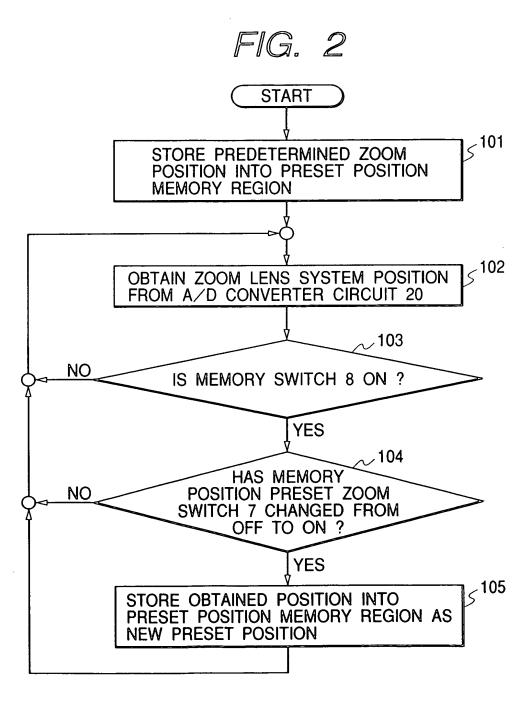
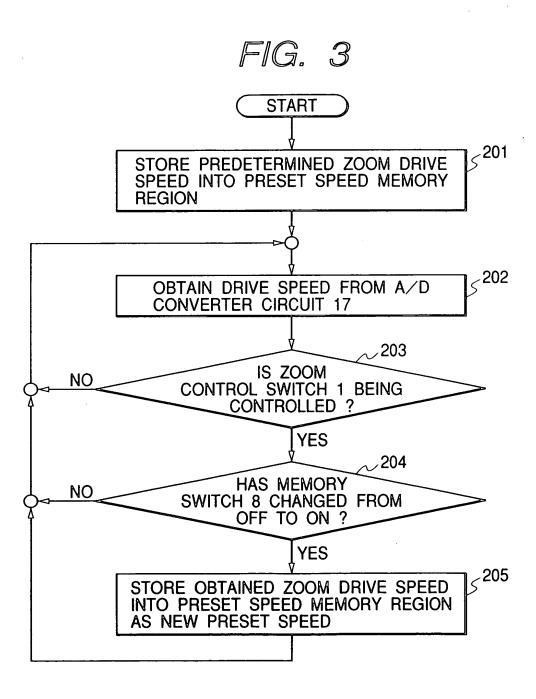
ó MEMORY CPU CPU COMMAND SIGNAL CALCULATION CIRCUIT A/D SPEED SIGNAL CALCUI CIRCUI





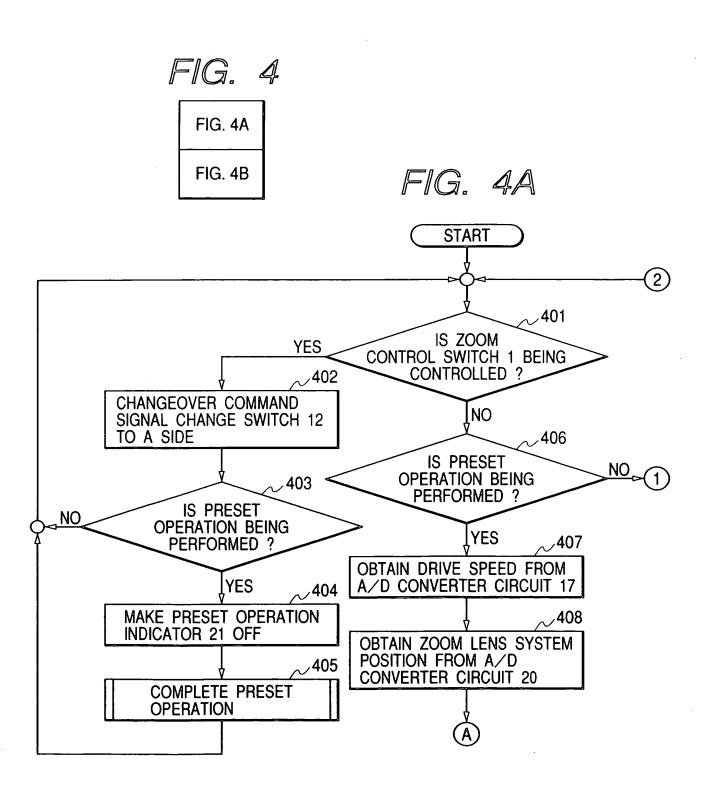
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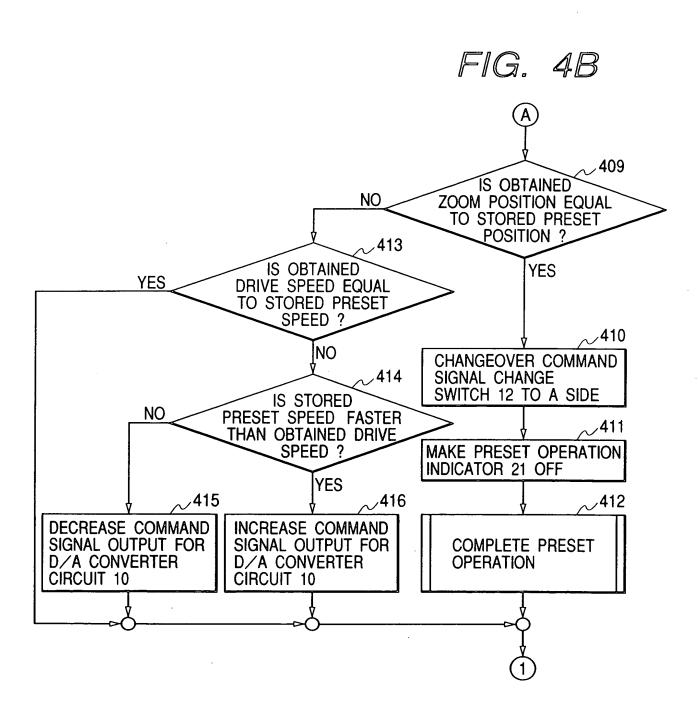
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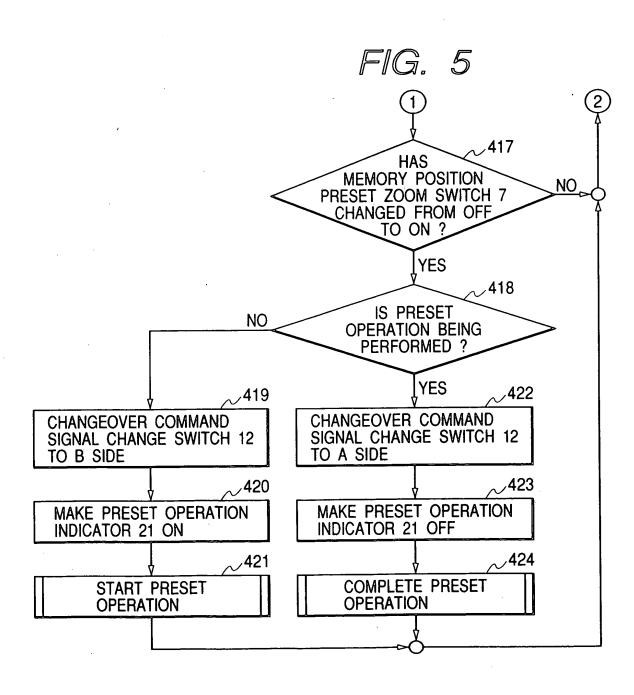


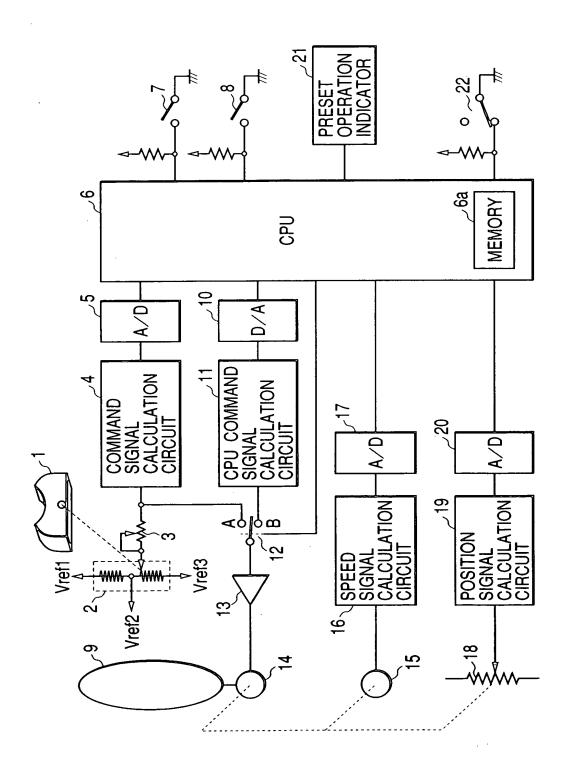


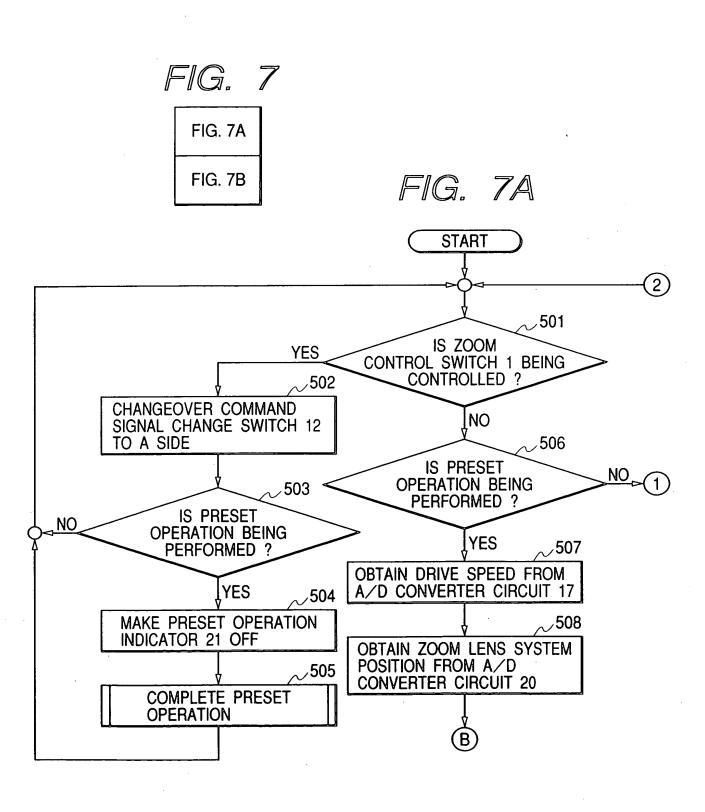
4/29

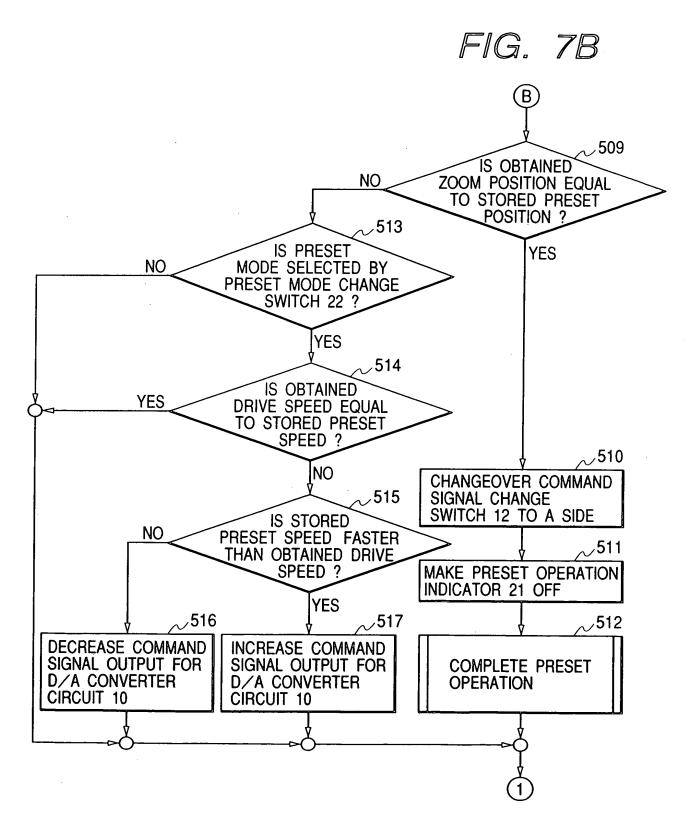








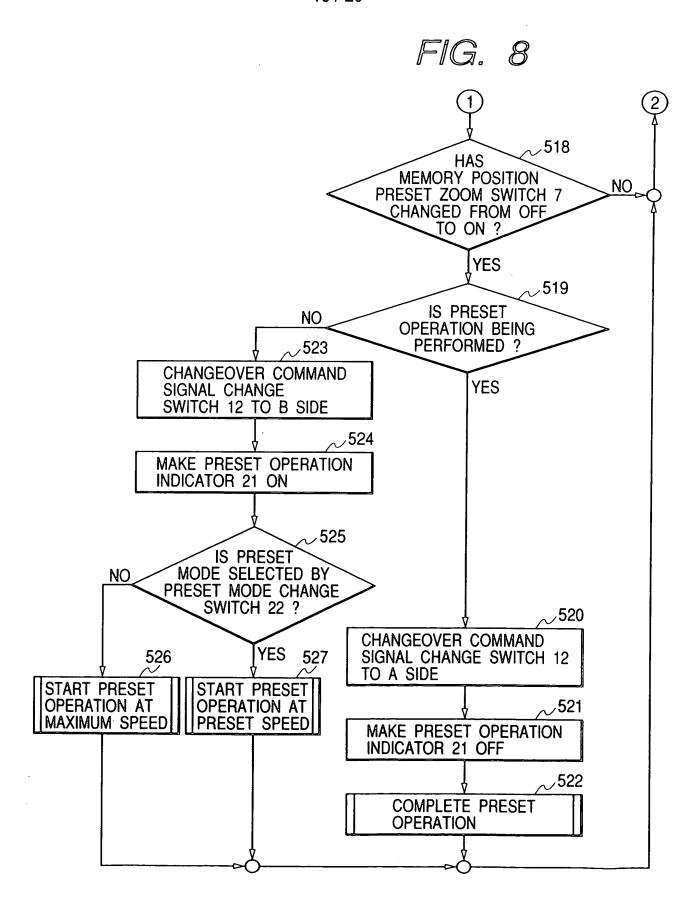








10 / 29



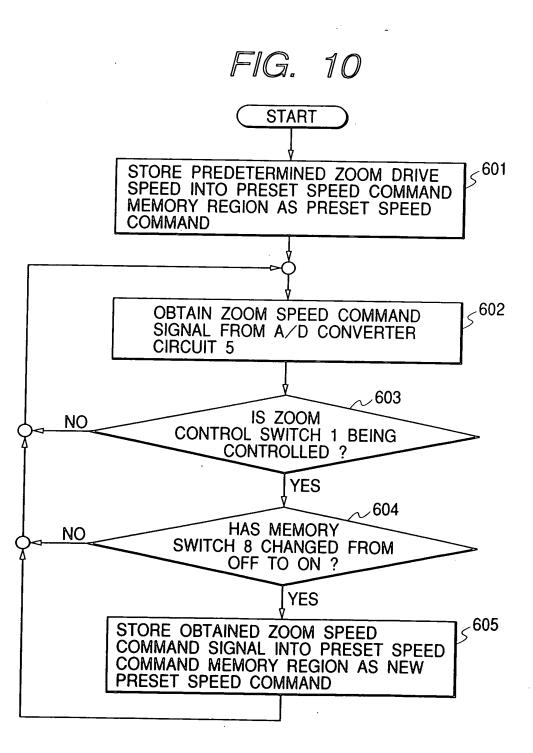


ó MEMORY CPU

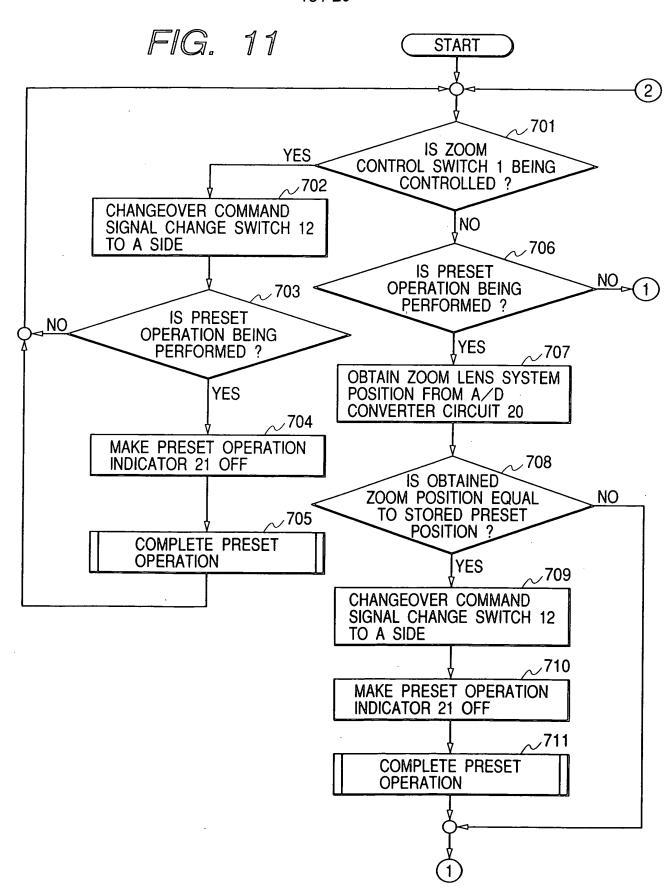




12/29



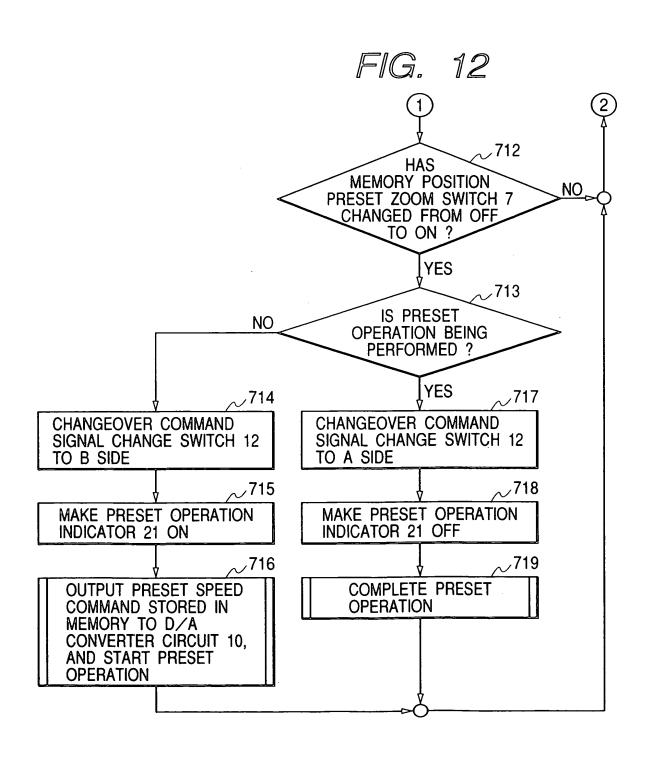
13/29



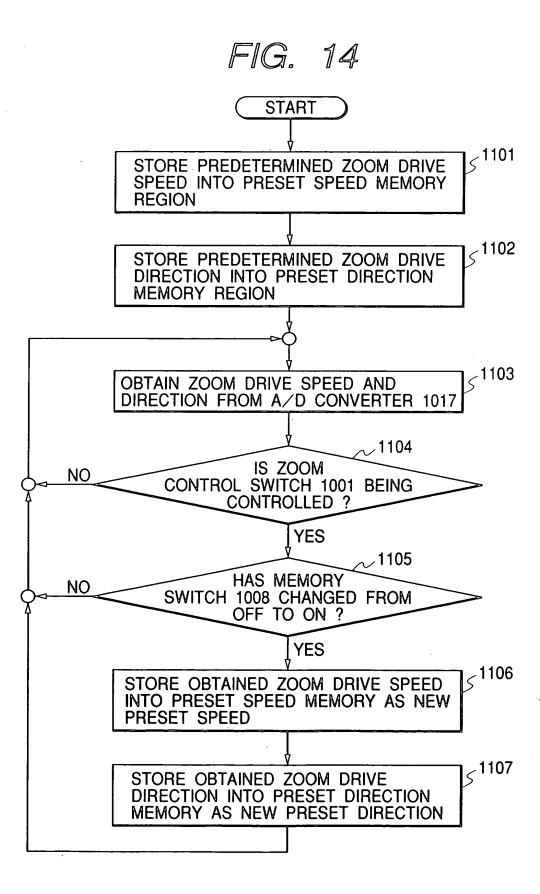


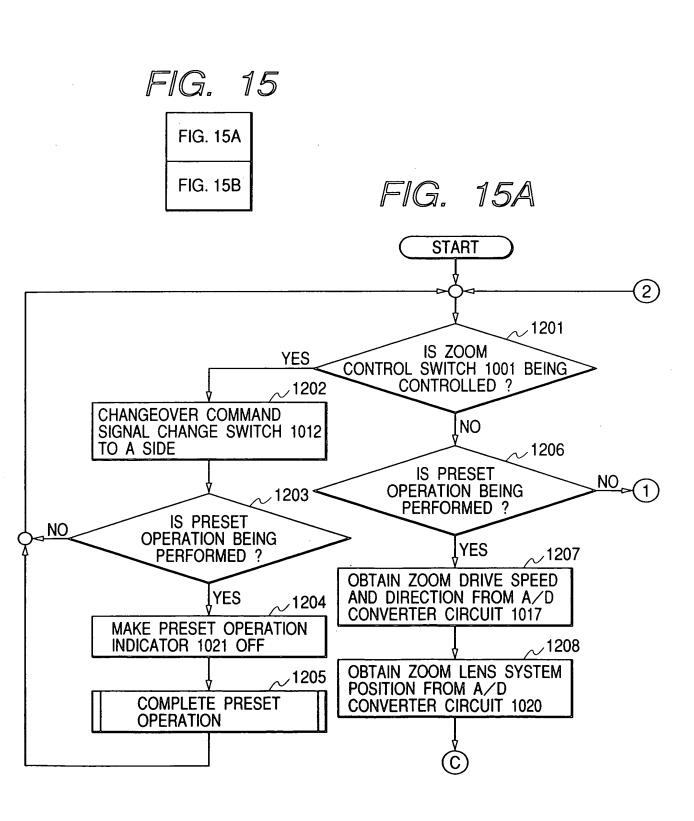


14/29

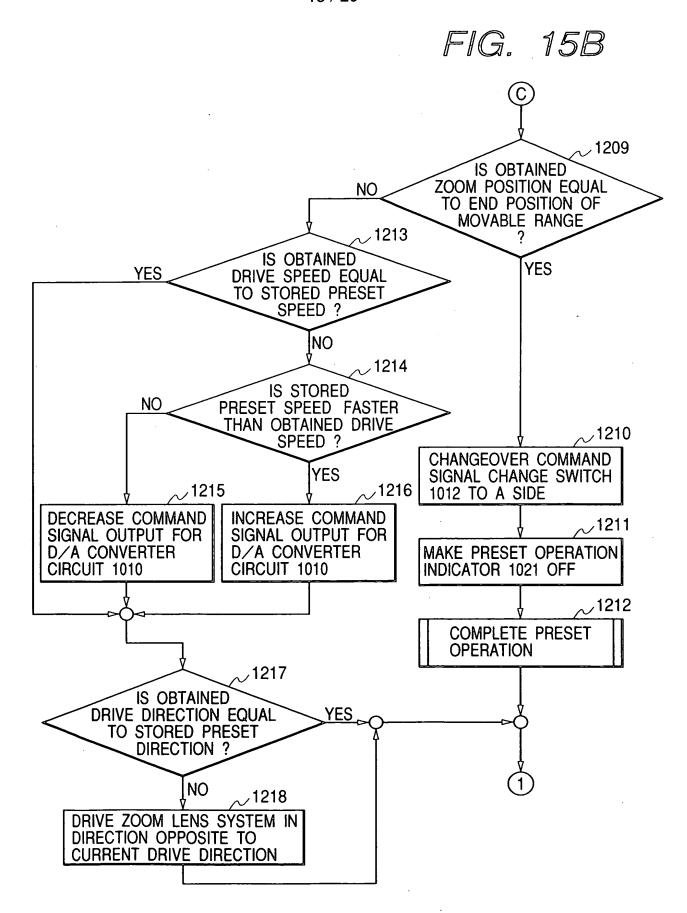


1006 MEMORY SP 1005 1010 FIG. 13 101 1020 1019 ဖြင့် POSITION SIGNAL CALCULAT SPEED SIGNAL CALCUI CIRCUI 1016





18 / 29



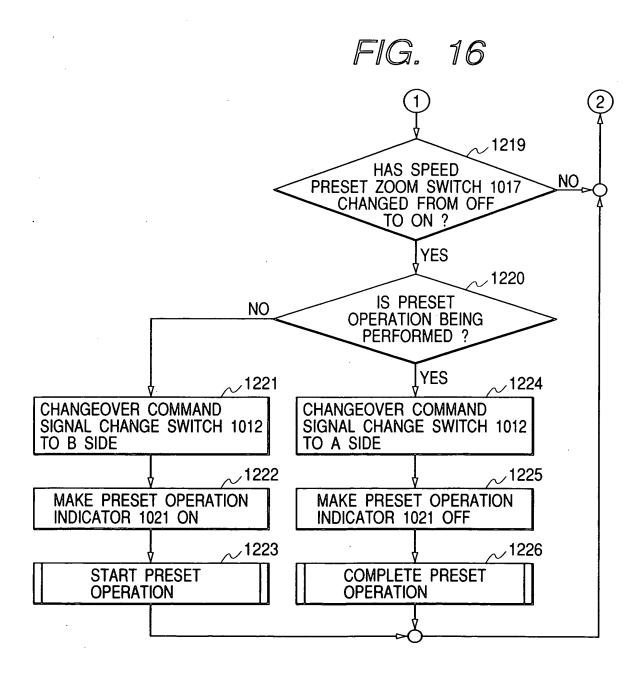
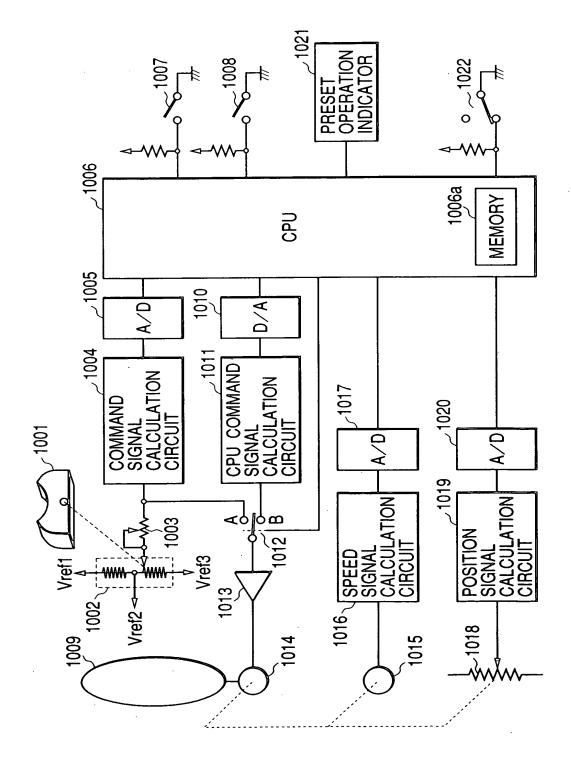


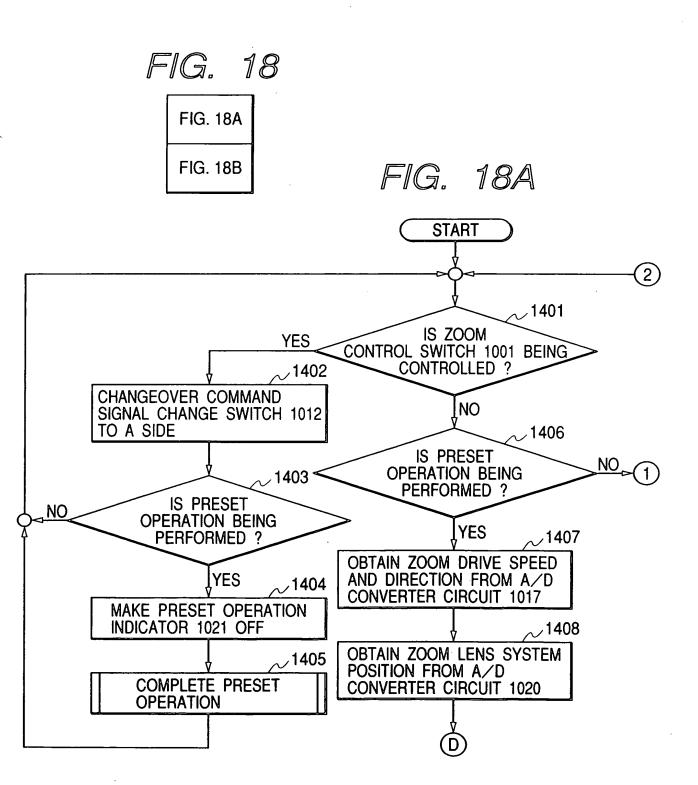
FIG. 17







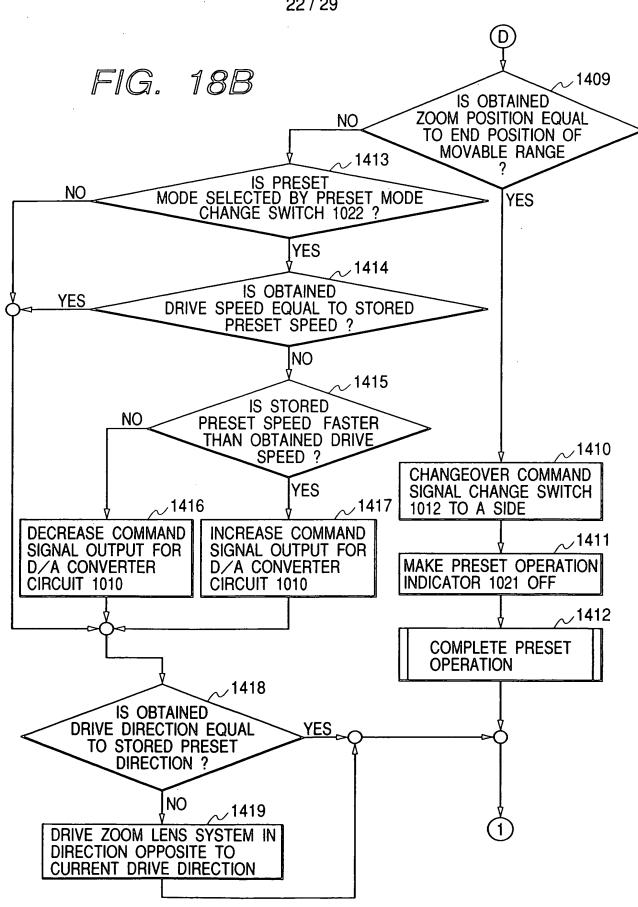
21 / 29

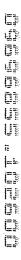












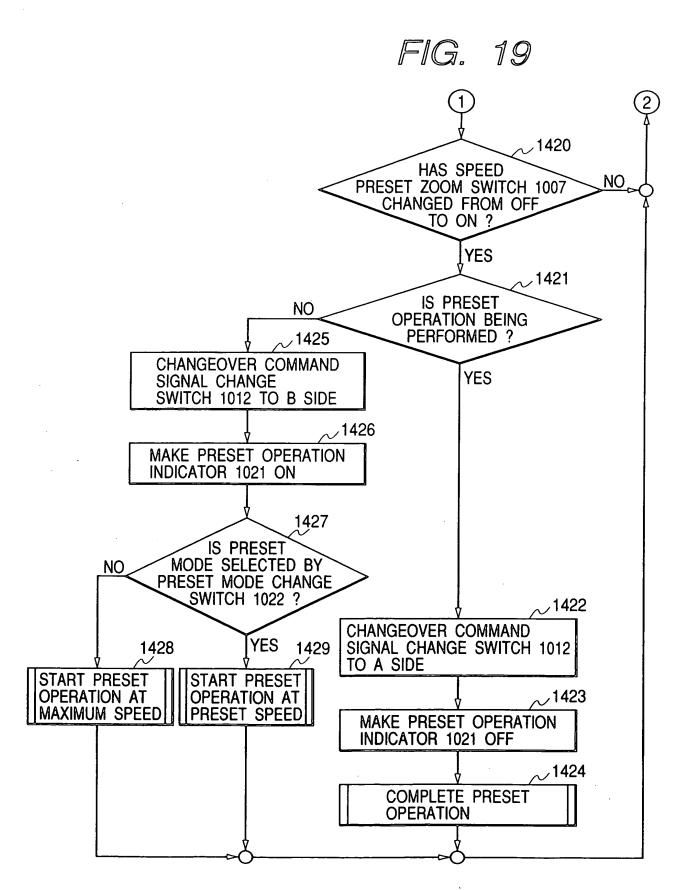
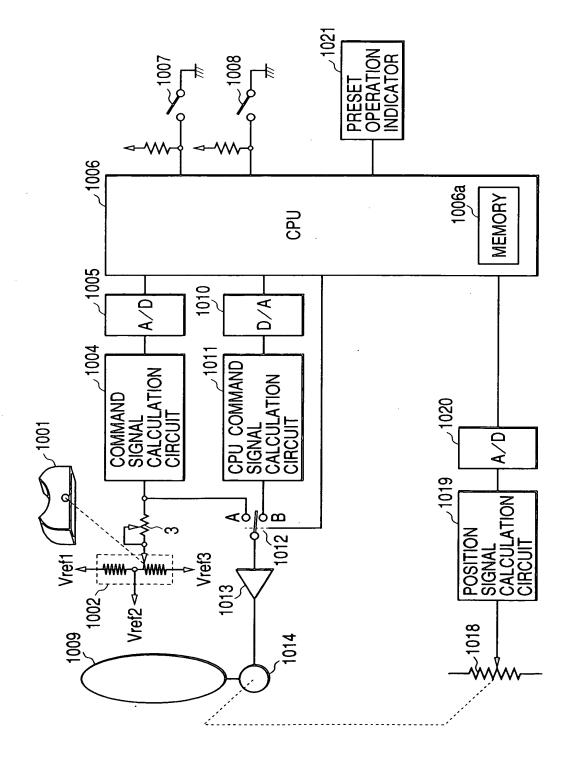


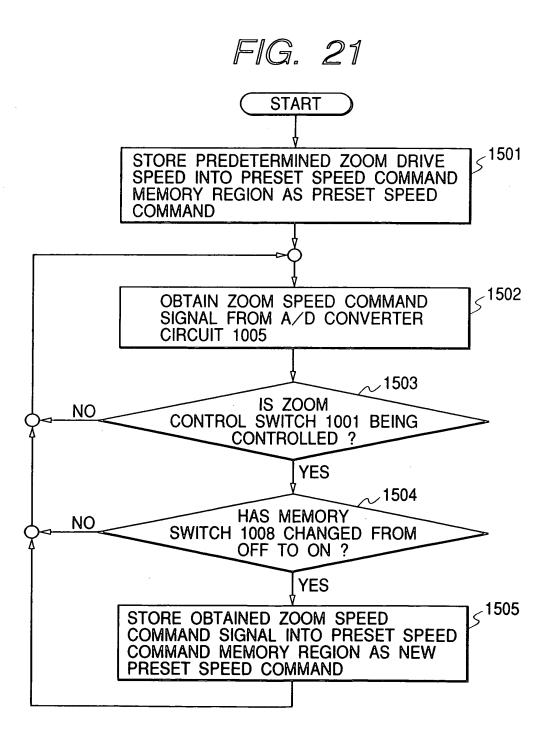
FIG. 20







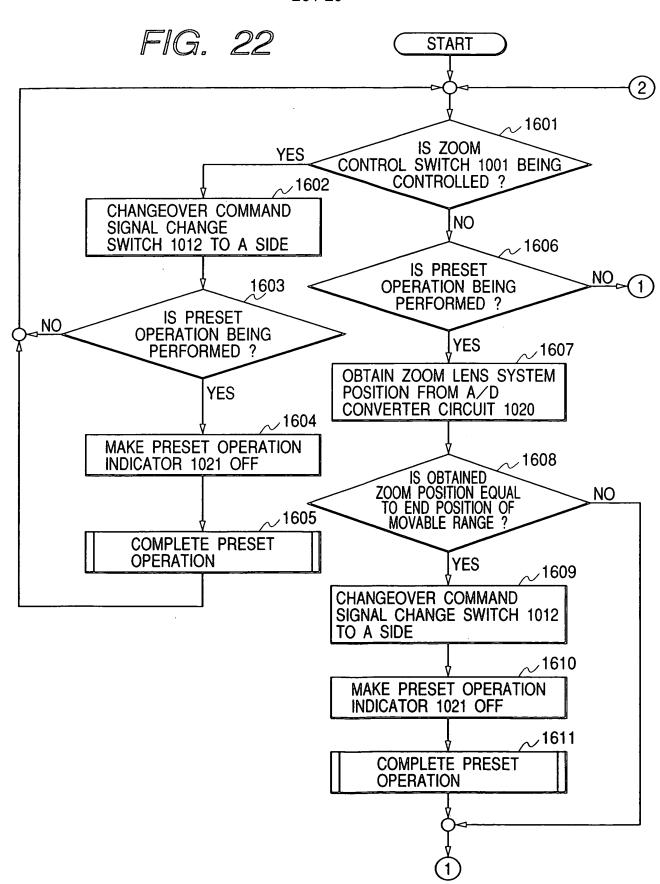
25 / 29







26 / 29







27 / 29

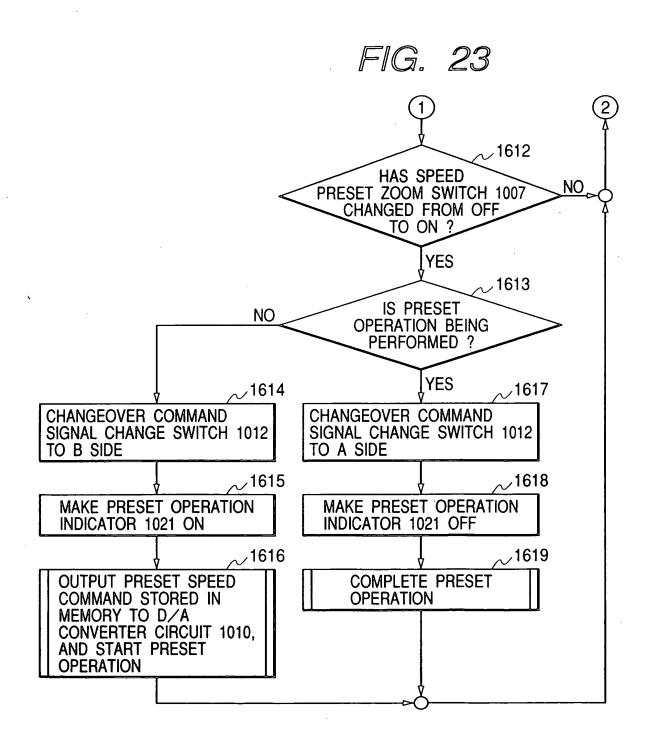
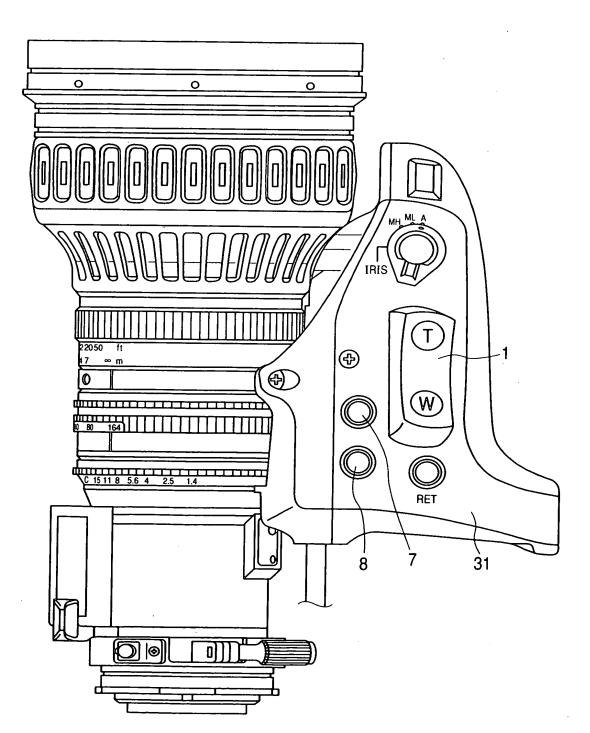


FIG. 24







29 / 29

FIG. 25

